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Signal Integrity, Electromagnetic Compatibility, Macromodeling, Digital Integrated Circuits, Transmission Lines, 3D Interconnects, Passivity 1 Introduction This paper presents a systematic methodology for the Signal Integrity and EMC simulation of distribution networks of digital signals in high-speed communication and information systems

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In the realm of high-speed digital design, signal integrity has become a critical issue, and is posing increasing challenges to the design engineers Many signal integrity problems are electromagnetic phenomena in nature and hence related to the EMI/EMC discussions in [1](#) Signal Integrity modeling for high-speed DDRx Using Chip ...

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