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~~Q. 5.10: A sequential circuit has two JK flip-flops A and B, two inputs x and y, and one output z~~

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Review Q. 5.6: A
sequential circuit with
two D flip-flops A and B,
two inputs, x and y; and
one output z is Q. 4.1:

~~Consider the
combinational circuit
shown in Fig. P4.1.(a)*~~

~~Derive the Boolean
expressions for~~ Digital
Design: Q. 1.13: Do the

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following conversion
problems: (a) Convert
decimal 27.315 to binary

Q. 5.8: Derive the state
table and the state
diagram of the sequential
circuit shown in Fig. P5.8

Digital Logic Design -
Unit 04 - Combinational
Logic - Part 03 - Design
Procedure Q. 5.4: A PN

flip-flop has four
operations: clear to 0, no
change, complement,

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and set to 1, when
DIGITAL SYSTEM
DESIGN OVERVIEW

Q. 6.10: Design a serial
 2^n 's complemeter with
a shift register and a
flip flop. The binary
number

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State Tables and
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diagram/state
table/circuit diagram
(using D-flip flop) -

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~~Sequential circuit design~~

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Sequential Circuit
Analysis - From
sequential circuit to state
transition diagrams.

What's

Inside?#17-Computer
System Architecutre by
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unboxing/unpacking Q.

7.19: Tabulate the PLA
programming table for

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the four Boolean functions listed below. Q. 3.13: Simplify the following expressions to (1) sum-of-products and (2) products-of-sums Q. 3.15: Simplify the following Boolean function F , together with the don't-care conditions d , and Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an

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inverter. Consider the
ECCE3206 Digital Logic
Design SQU ERT

Lecture Sequential

Circuits L03 Q.5.20:

Design the sequential
circuit specified by the
state diagram of Fig. 5.19
using T flip-flops Q. 3.9:

Find all the prime
implicants for the
following Boolean
functions, and determine
which are digital-circuit

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circuits are employed in
the design of systems
such as digital
computers, control
systems, data
communications, and
many other applications

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Experimental Procedure:

Task 1: Our first task was to create a logic diagram of a 4-bit binary adder, which consisted of just one chip titled

“ 7483N ” , along with creating a truth table. I connected the A1-A4 to either the voltage or ground pins to keep a constant value (1001) to

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